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Fundamentals of Digital Logic with Verilog Design, by Brown & Vranesic, 2nd ed (2012), ISBN 978-0-07-066724-2. This is described as a 2012 edition but is actually an incomplete version of the 2008 edition. (It is in fact the 2012 Special Indian Edition.) The twelve chapters and Appendix A, up to page 822, seem to be identical copies of the 2008 edition.

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